



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 5687

Bib Data Sheet

SERIAL NUMBER 10/716,556	FILING OR 371(c) DATE 11/20/2003 RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. 245469US2DIV
APPLICANTS Tadashi Iguchi, Yokkaichi-Shi, JAPAN; Yoshiaki Himeno, Yokkaichi-Shi, JAPAN; Hiroaki Tsunoda, Yokkaichi-Shi, JAPAN;				
** CONTINUING DATA ***** This application is a DIV of 09/732,723 12/11/2000 PAT 6,720,610				
** FOREIGN APPLICATIONS ***** JAPAN 11-350841 12/09/1999				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/27/2004				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 22	TOTAL CLAIMS 9
INDEPENDENT CLAIMS 3				
ADDRESS 22850				
TITLE Method of manufacturing a nonvolatile semiconductor memory device having a stacked gate structure				
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	

4/1/05